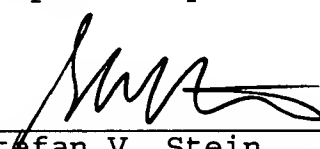


REMARKS

Claims 1-11 presently stand rejected under 35 U.S.C. 102(a) as being anticipated by Whitaker or Pickett. In order to more particularly define what Applicant regards as her invention, Applicant has cancelled original Claims 1-11 and inserted new Claims 12-21 therefor. New Claims 12-21 include specific limitations to reconfiguring the memory array to one that has a word with of "k" bits wide. This ability to reconfigure the word with of the memory array is not taught by either of Whitaker or Pickett.

In the case of Whitaker, the use of PASS transistors is taught for reconfigurability, but there is no teaching of reconfiguring a memory array to achieve different word widths. In the case of Pickett, memory paging is taught, similar to that referenced in the Background of the Invention, but again there is no teaching of reconfiguring a memory array to achieve different word widths. Therefore, the 102(a) rejection is respectfully traversed and reconsideration is respectfully requested.

Respectfully submitted,



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CERTIFICATE OF MAILING

I HEREBY CERTIFY that the foregoing was placed in an envelope and mailed U.S. First Class Mail, postage paid to the Commissioner of Patents and Trademarks, Washington, D.C. 20231 on this the 9th day of February, 1998.

The Commissioner is hereby authorized to charge any additional fees which may be required at any time during the prosecution of this application without specific authorization, or credit any overpayment, to Deposit Account Number 19-4127.



Stefan/V. Stein